

DALSA CA-D4/D7 Cameras

Designed for the most demanding imaging challenges, the DALSA CA-D4 and CA-D7 cameras offer superior performance. 1024x1024 progressive scan, 40 frames per second, outstanding sensitivity and extremely low dark current make the CA-D4 and CA-D7 the choice of OEMs and professionals around the world.

Features

- 1024x1024 resolution, 12µm square pixels
- 100% fill factor
- Frame transfer architecture and pixel reset/exposure control—no shutter required
- One or two outputs
- 25MHz (8-bit) or 10MHz (12-bit) per output data rate
- Vertical and horizontal binning options
- "Snapshot" operation
- CE-compliant version of A model available

Description

The CA-D4/D7 cameras offer 1024 x 1024 resolution with switch-selectable single or dual 8-bit outputs (A model) or a single 12-bit output (T model).

Data for the 8-bit models is provided at 25MHz per output. This results in frame rates up to 21 frames/sec with a single output or 40 frames/sec with dual outputs. Data for the 12-bit model is provided at 10MHz. This results in frame rates up to 8 frames/sec.

The CA-D4/7 cameras use DALSA's patented modular architecture. This system of connecting circuit modules through standardized busses allows DALSA to build a high performance modular camera using the reliability, flexibility, and cost-effectiveness of high-volume interchangeable parts. Within the camera, a driver board provides bias voltages and clocks to the CCD image sensor, a timing board generates all internal timing, and A/D boards process the video and digitize it for output. Contact DALSA for further information.

Applications

The CA-D4 /D7 are ideally suited for use in:

- Wafer and Electronic Inspection
- Medical Imaging
- Microscopy



Table 1. Camera Configurations

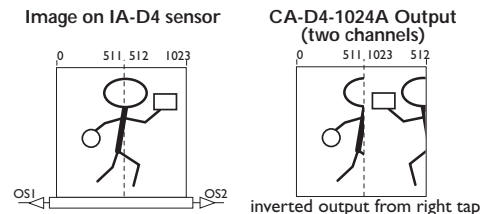
Specification	A Model	T Model
Pixel Pitch	12µm	12µm
Aperture	12.3 x 12.3mm	12.3 x 12.3mm
Lens Mount	F-mount	F-mount
Data Bits	8 per channel	12
Data Rate	1 or 2 x 25MHz	1 x 10MHz
Max. Frame Rate	40fps	8fps
Data Format	RS422	RS422
CE model available	Yes (code A-ECE)	No
Data Channels	1 or 2	1

Example Configuration: CA-D4-1024A

resolution _____ model

Sensor

The CA-D4/D7 cameras use the IA-D4 area scan image sensor for high speed, high resolution performance. The sensor uses a frame transfer architecture. The pixel size is 12µm x 12µm with a 100% fill factor (all of the image area is photo-sensitive) to achieve maximum sensitivity within the pixel. Figure 6 shows the block diagram of the sensor. The sensor uses one output in the CA-D7-1024T camera and one or two outputs in the CA-D4-1024A. When reading out pixels to a single output, the sensor starts at pixel column 1 and proceeds to column 1024; but when using two outputs, the sensor simultaneously reads out columns 1 to 512 to OS1 and columns 1024 to 513 to OS2. This output effectively mirrors the right half of an image as shown below.



Operation

Power Supplies

Cameras provide optimum performance using well-regulated linear supplies. The power supply requirements indicated here are adequately overrated to accommodate all models and operating conditions.

Voltage	Current Draw	Notes
+28V	250mA	Specified at 40°C ambient. Tolerance for all supplies: ■ ± 50mV ■ <5mV ripple.
+5V	1500mA	
-5V	225mA	
-15V	50mA	

Optical Interface

The CA-D4/D7 cameras provide an adapter for an F-mount (35mm Nikon-compatible) lens. This mount threads into an opening in the camera's front plate, and is optically aligned to provide the proper back focal distance between F-type lens and sensor. The threaded hole can also be used to provide custom optical mounts. The image sensor's frame transfer architecture and its pixel reset feature mean the camera does not require a shutter for most applications.

Electrical Interface

All of the CA-D4/D7 connectors are on the rear plate. The power and control signals are input and output from a DB25 connector. For 8-bit models, digital data and clocking signals are provided on two 20-pin IDC connectors. CE-compliant models use DB25 data connectors. The CA-D7 T model uses a single 40 pin IDC connector. See Figures 1-3.

All control, digital data and clocking signals use the RS422 (differential) standard for data transmission.

CE Compliance

The CA-D4-1024A-ECE camera meets the following EMC standards:

- EN 50082-1
- IEC 1000-4-2;1995
- IEC 1000-4-3;1995
- IEC 1000-4-4;1995
- CISPR-22

Input Control Signals

The CA-D4/D7 cameras require only one input signal. The transfer clock EXSYNC is required to trigger frame readout. Note that for optimum performance, the cameras use an internal oscillator to control all internal timing, including pixel rate. **These cameras do not accept an external MCLK signal.**

Control signals are differential, requiring complements denoted with a "B" suffix (e.g. EXSYNC, EXSYNCB).

Signal	Controls
EXSYNC	Start of frame readout, Frame rate, Integration/Exposure time
PRIN	Pixel Reset, Effective exposure time
BIN	Pixel Binning

EXSYNC

EXSYNC is a required user-supplied input signal used to trigger frame readout, thereby controlling the camera frame rate and integration time. The camera integrates from falling edge of EXSYNC to the next falling edge of EXSYNC.

EXSYNC State	Camera Mode
High	Integration
Falling Edge	Frame readout begins
Held low	Maximum frame rate

PRIN (Exposure Control)

PRIN is an optional user-supplied input signal that can be used to control exposure by altering exposure time independently of EXSYNC.

PRIN State	Camera Mode
Low	Pixel reset (no exposure)
Rising Edge	Exposure begins
High	Exposure
Unconnected	Exposure

BIN

BIN is an optional user-supplied input signal. BIN high causes the camera to bin its pixels 2x2 vertically and horizontally to provide 512x512 resolution, increasing sensitivity while reducing resolution. **If you do not use BIN, tie BIN low and BINB high.**

USR_EN

USR_EN is an optional input signal located on the OS2 connector (A model only). It can be used in special operating modes to select a camera from multiple cameras multiplexed onto a common data bus.

USR_EN State	Camera Mode
Low	Camera outputs tri-stated
High	Camera outputs active
Unconnected	Camera outputs active

Output Signals

These signals indicate when data is valid, allowing you to clock camera data into your acquisition system:

Clocking Signal	Indicates
FVAL (high)	Valid frame
LVAL (high)	Valid line
STROBE (falling edge)	Valid pixel

Digital Data

The CA-D4-1024A outputs one or two channels of 8-bit RS422 data at 25MHz. The CA-D7-1024T models output 12-bit RS422 data. See Figures 2-4 for pinouts.

Snapshot Operation

By using a combination of EXSYNC and PRIN inputs, the camera can operate in "snapshot" mode, in which image capture is triggered by an external event with up to one minute between image captures. Note that the EXSYNC and PRIN signals must be triggered by another part of your acquisition system. For an application note on this topic, contact DALSA and request doc# 03-32-00220.

Figure 1. Camera Interface, A Model

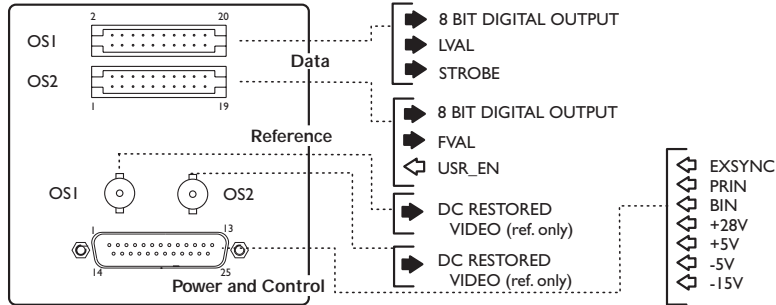


Figure 2. Camera Interface, T Model

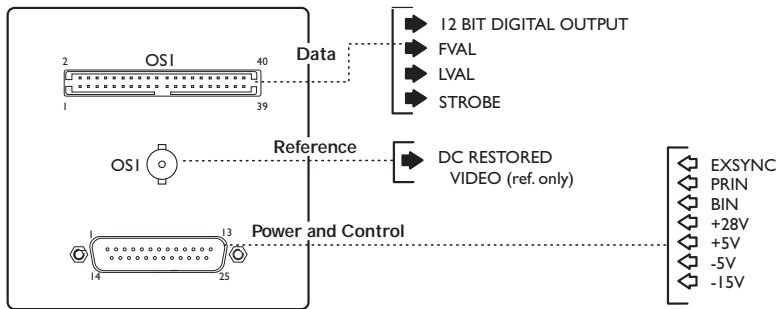
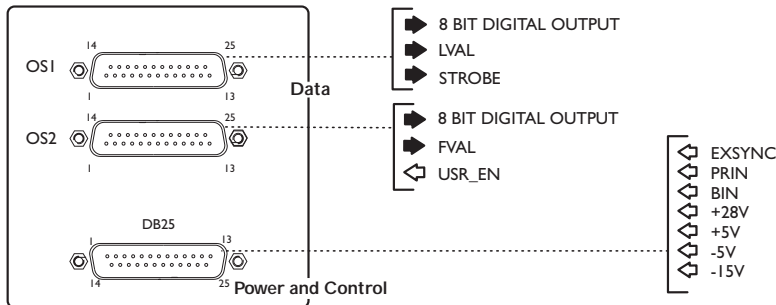


Figure 3. Camera Interface, CE-Compliant A Model



ISO 9001 DALSA maintains a registered quality system meeting the ISO 9001 standard.

Life Support Applications

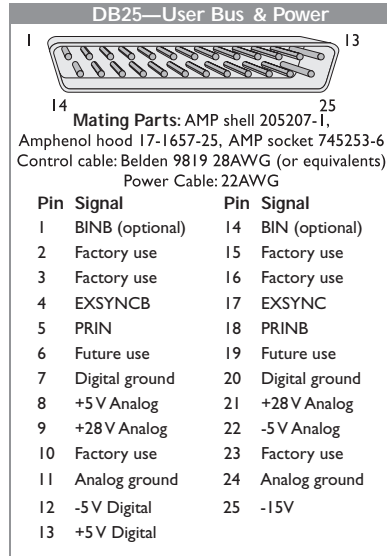
These products are not designed for use in life support appliances, devices, or systems where malfunction of these products can reasonably be expected to result in personal injury. DALSA customers using or selling these products for use in such applications do so at their own risk and agree to fully indemnify DALSA for any damages resulting from such improper use or sale.

Figure 4. Data Connector Pinouts

OS1 — Data & Clocking (A models)				OS2'—Data & Clocking (A models)				Notes																																																																																																																			
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Note: Do not connect to "Factory use," "Future use," or "No connect" pins.

Figure 5. Control and Power Connector Pinout



Note: Do not connect to "Factory use," "Future use," or "No connect" pins.

Figure 6. Sensor Block Diagram

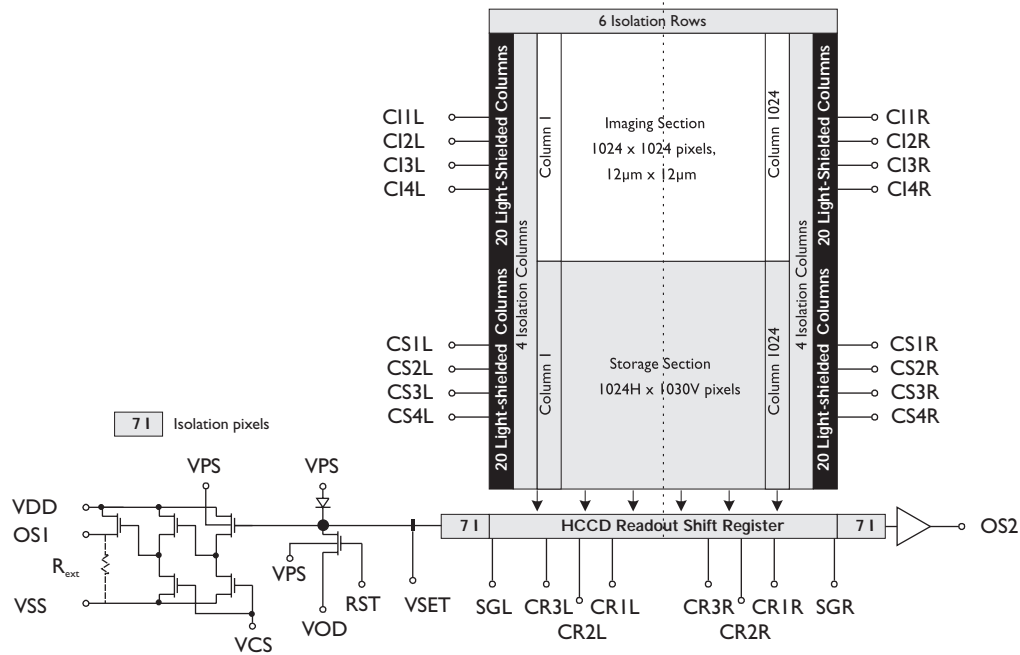
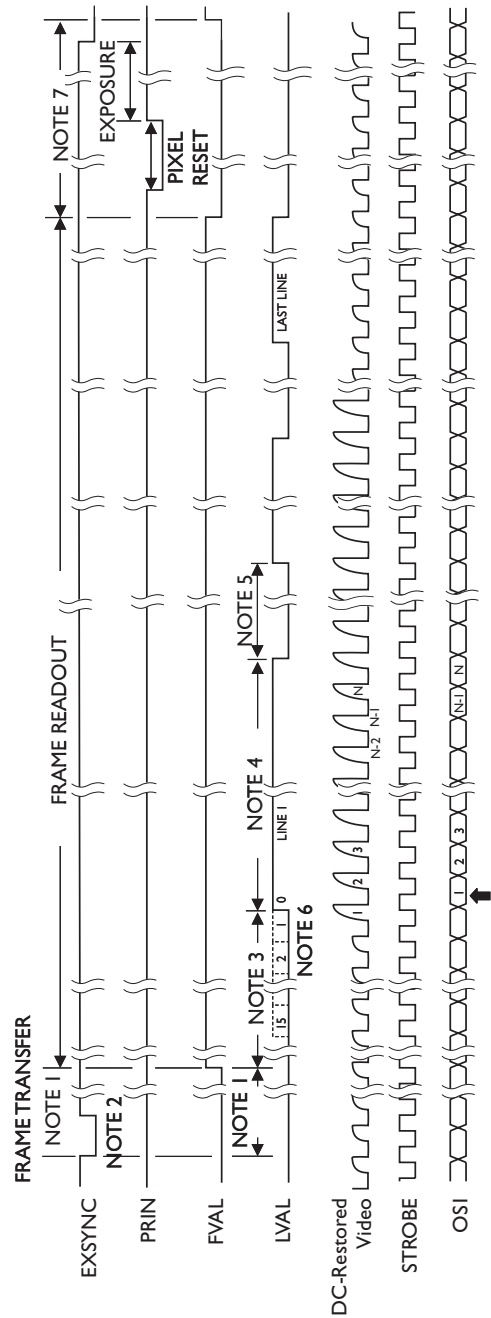
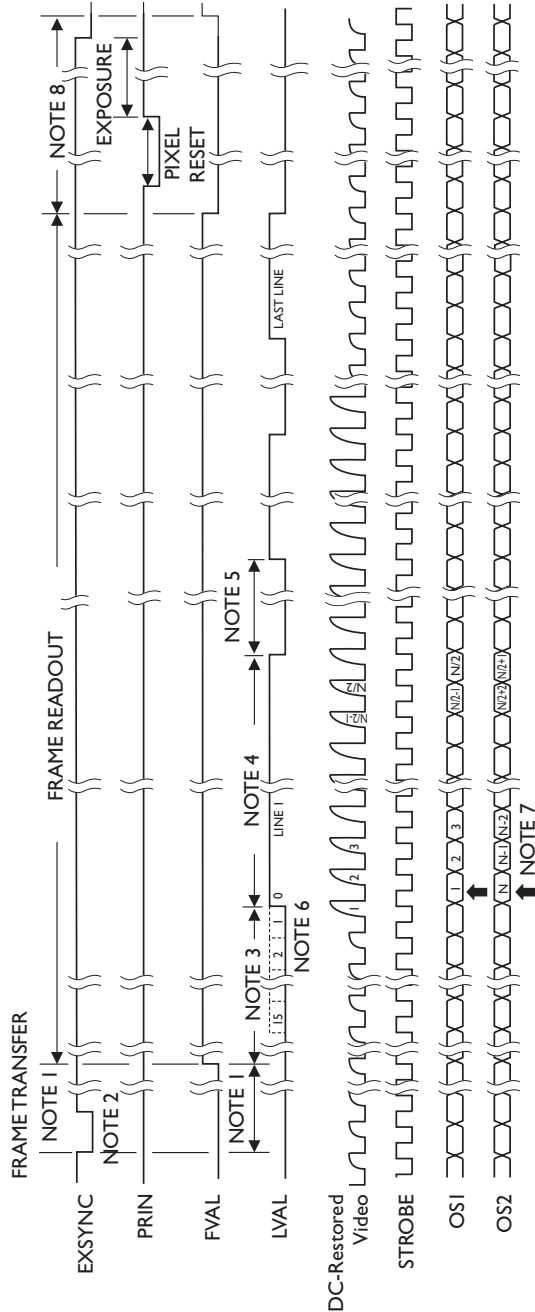


Figure 7. CA-D4 A Camera Timing, One Output



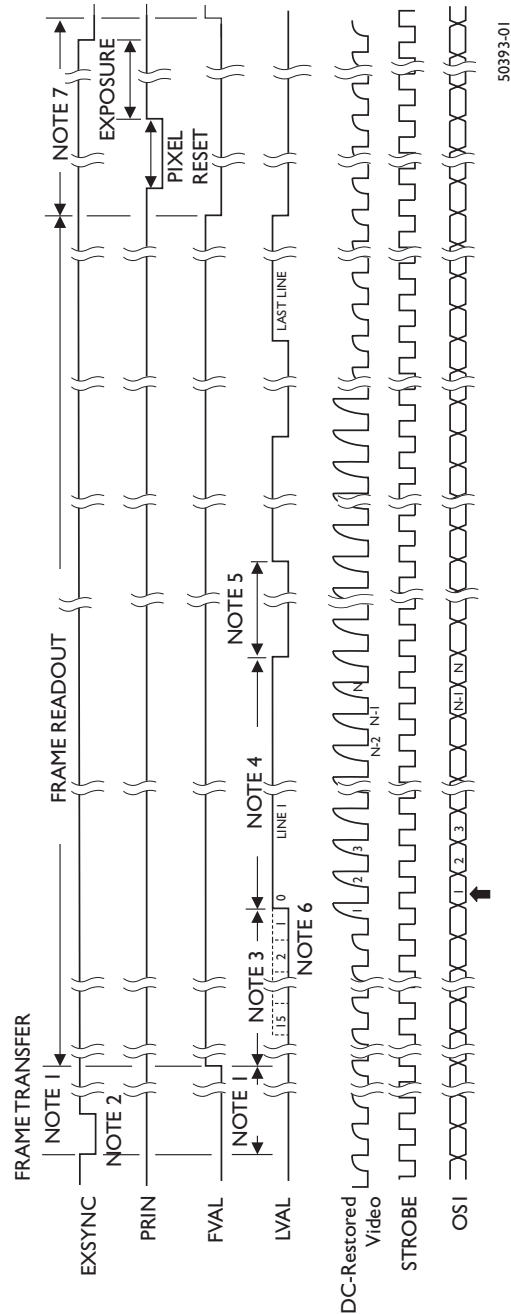
- NOTES:** N = 1024 (2x2 Binning mode: N = 512)
- EXSYNC to FVAL rising edge A model = 1.012 ms at 25 MHz.
EXSYNC to first valid pixel A model = 25337 ± 25 STROBES. 2x2 Binning mode: 12680 ± 12 STROBES.
 - EXSYNC minimum pulse width = 100 ns.
 - FVAL to LVAL = 31-(pretrigger) STROBES. 2x2 Binning mode: 15-(pretrigger) STROBES.
 - Line readout: LVAL stays logic HIGH for 1024 STROBES. 2x2 Binning mode: 512 STROBES.
 - Between lines: LVAL stays logic LOW for 101-(pretrigger) STROBES. 2x2 Binning mode: 62-(pretrigger) STROBES.
 - Dashed lines indicate LVAL position with pretrigger. Pretrigger is user-selectable in 1-pixel increments from 0-15.
 - Minimum time between frames: 32500±25 STROBES. 2x2 binning mode: 13750±10 STROBES.
- ↑ Position of first valid pixel.

Figure 8. CA-D4 A Camera Timing, Two Outputs



- NOTES:** N = 512 (2x2 Binning mode; N = 256)
1. EXSYNC to FVAL rising edge A model: 1.012 ms at 25 MHz.
EXSYNC to first valid pixel A model: 25337 ± 25 STROBES. 2x2 Binning mode: 12680 ± 12 STROBES.
 2. EXSYNC minimum pulse width = 100 ns.
 3. FVAL to LVAL = 31-(pretrigger) STROBES. 2x2 Binning mode: 15-(pretrigger) STROBES.
 4. Line readout: LVAL stays logic HIGH for 512 STROBES. 2x2 Binning mode: 256 STROBES.
 5. Between lines: LVAL stays logic LOW for 70-(pretrigger) STROBES. 2x2 Binning mode: 47-(pretrigger) STROBES.
 6. Dashed lines indicate LVAL position with pretrigger. Pretrigger is user-selectable in 1-pixel increments from 0-15 (1-15 for B model).
 7. On OS2, A models output pixels from 1024 to 513 (2x2 Binning mode: 512 to 257); B models output 513-1024 (2x2 Binning mode: 257 to 512).
 8. Minimum time between frames: 27500±25 STROBES. 2x2 binning mode: 12500±10 STROBES.
- ↑ Position of first valid pixel.

Figure 9. CA-D7 T Camera Timing



50393-01

NOTES: N = 1024 (2x2 Binning mode: N = 512)

1. EXSYNC to FVAL rising edge T model = 2.531 ms at 10 MHz.
EXSYNC to first valid pixel T model = 25337 ± 25 STROBES. 2x2 Binning mode: 12680 ± 12 STROBES..
 2. EXSYNC minimum pulse width = 100 ns.
 3. FVAL to LVAL = 31-(pretrigger) STROBES. 2x2 Binning mode: 15-(pretrigger) STROBES.
 4. Line readout: LVAL stays logic HIGH for 1024 STROBES. 2x2 Binning mode: 512 STROBES.
 5. Between lines: LVAL stays logic LOW for 101-(pretrigger) STROBES. 2x2 Binning mode: 62-(pretrigger) STROBES.
 6. Dashed lines indicate LVAL position with pretrigger. Pretrigger is user-selectable in 1-pixel increments from 0-15.
 7. Minimum time between frames: 32500±25 STROBES. 2x2 binning mode: 13750±10 STROBES.
- ↑ Position of first valid pixel.

Table 2. CA-D4/D7 Performance Specifications

Specification	Units	A Models (incl. ECE)	T Model
Per Output Data Rate, max.	MHz	25	10
Frame Rate, max.	Hz	40	8.4
Saturation Output Amplitude	DN	255	4095
Light Intensity at Saturation,	single output	$\mu\text{W}/\text{cm}^2$	1.80
	dual output	$\mu\text{W}/\text{cm}^2$	3.41
Output Gain Mismatch, max.	%	3.0	1.0
DC Offset, binning disabled, max.	DN	8.0	60
DC Offset, binning enabled, max.	DN	8.0	120
Photoresponse Non Uniformity (PRNU) rms, max. ^{1,3}		%	3.0
	PRNU rms, max. ^{2,3}	%	4.0
Fixed Pattern Noise (FPN) rms, binning disabled, max.		DN	2.0
	FPN rms, binning enabled, max.	DN	2.0
Random Noise	Peak to peak, max.	DN	5
	rms, max	DN	0.9
Noise Equiv. Exposure (NEE), typ.	$\mu\text{J}/\text{cm}^2$	170	48
Saturation Equiv. Exposure (SEE), typ.	nJ/cm^2	86	127
Responsivity, typ.	DN/(nJ/cm^2)	2.9	31
Dynamic Range, min.		ratio	280:1
	Dynamic Range, typ.	ratio	500:1
Operating Temperature, max. ambient	$^{\circ}\text{C}$	40	40

Notes:

- DN = Digital Numbers (0-255 for 8-bit system).
- See Camera Measurement Definitions (doc# 03-36-00056) for specification definitions.
 1. With exposure control disabled.
 2. With exposure control enabled.
 3. Includes cosmetic defects.

Test Conditions

- All measurements exclude perimeter 16 rows and columns.
- All measurements exclude column defects.
- Tungsten halogen light source, black body color temperature 3200K, filtered with 750nm IR cutoff filter.

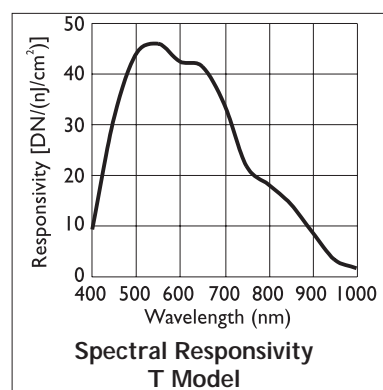
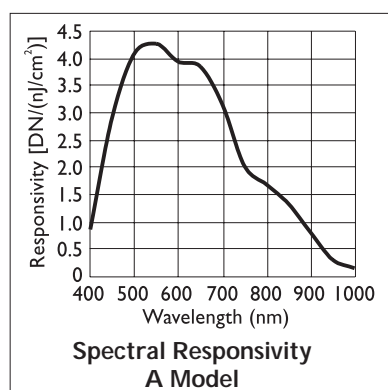
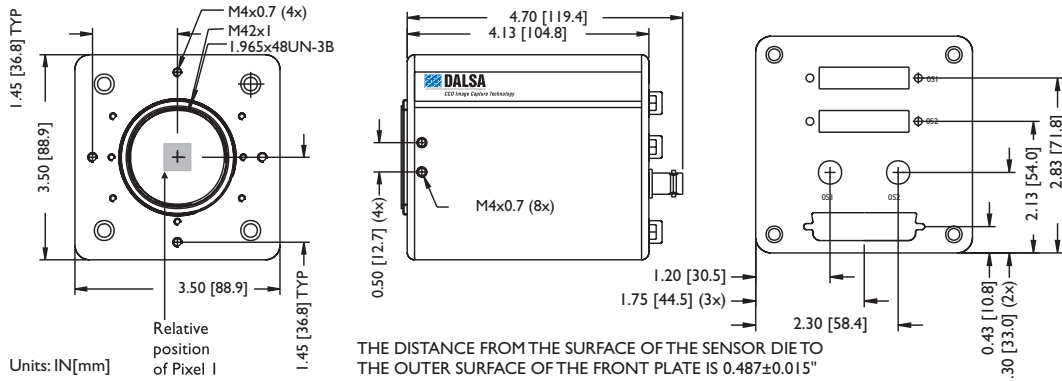
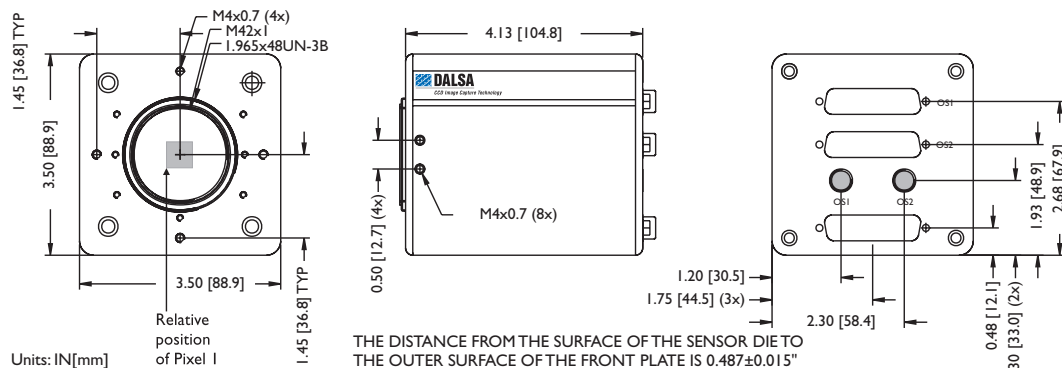


Figure 10. Mechanical Dimensions

A Model



CE-Compliant A Model



T Model

